

Figure 1  
(PRIOR ART)

FIG. 1A is a block diagram of a system 150. The system 150 includes a Data Source 160, a RX/Formatter 152, a TX/Formatter 158, a RAM 156, a  $\mu$ P 154, and a Graphic Display Device 162. The Data Source 160 is connected to the RX/Formatter 152. The RX/Formatter 152 is connected to the TX/Formatter 158. The TX/Formatter 158 is connected to the Graphic Display Device 162. The RX/Formatter 152, TX/Formatter 158, RAM 156, and  $\mu$ P 154 are all connected to a common bus 159.

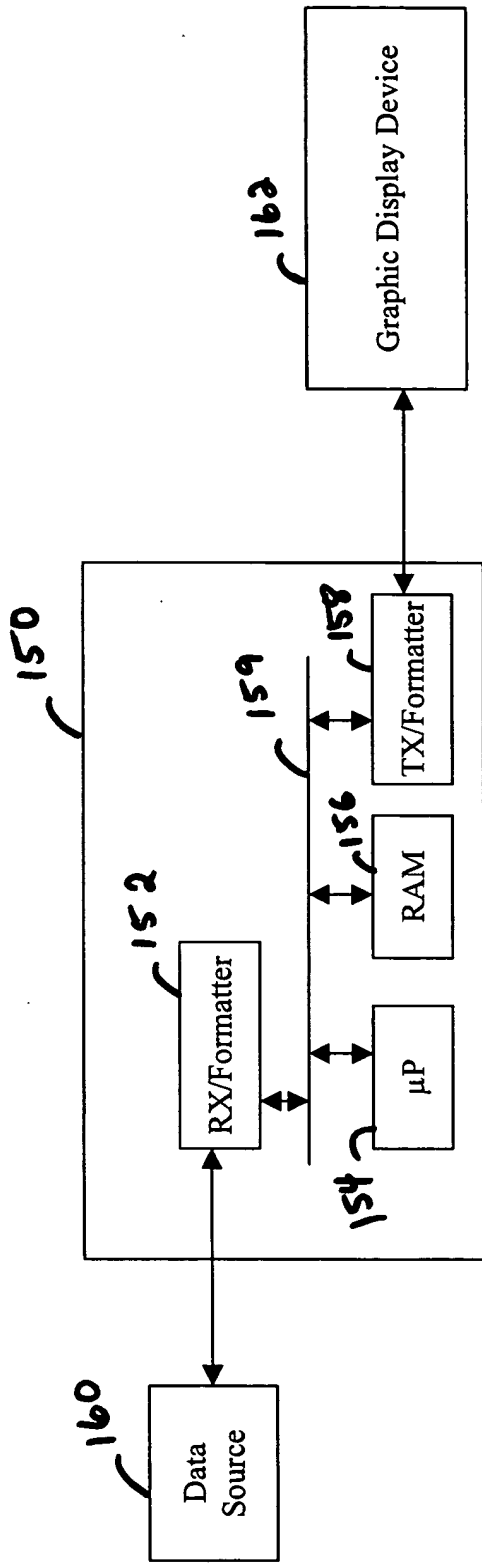


Figure 1A

208a	Partial EIT (2 current 3-hour segments; high spool rate)	202
206	Full EIT (54 remaining 3-hour segments; low spool rate)	204
208b	Service 1 (Audio and Video)	
:	:	
208n	Service N (Audio and Video)	

Figure 2

SRV0	1	6	11
SRV1	2	7	12
SRV2	3	8	13
SRV3	4	9	14
SRV4	5	10	15

TIME →

Figure 3

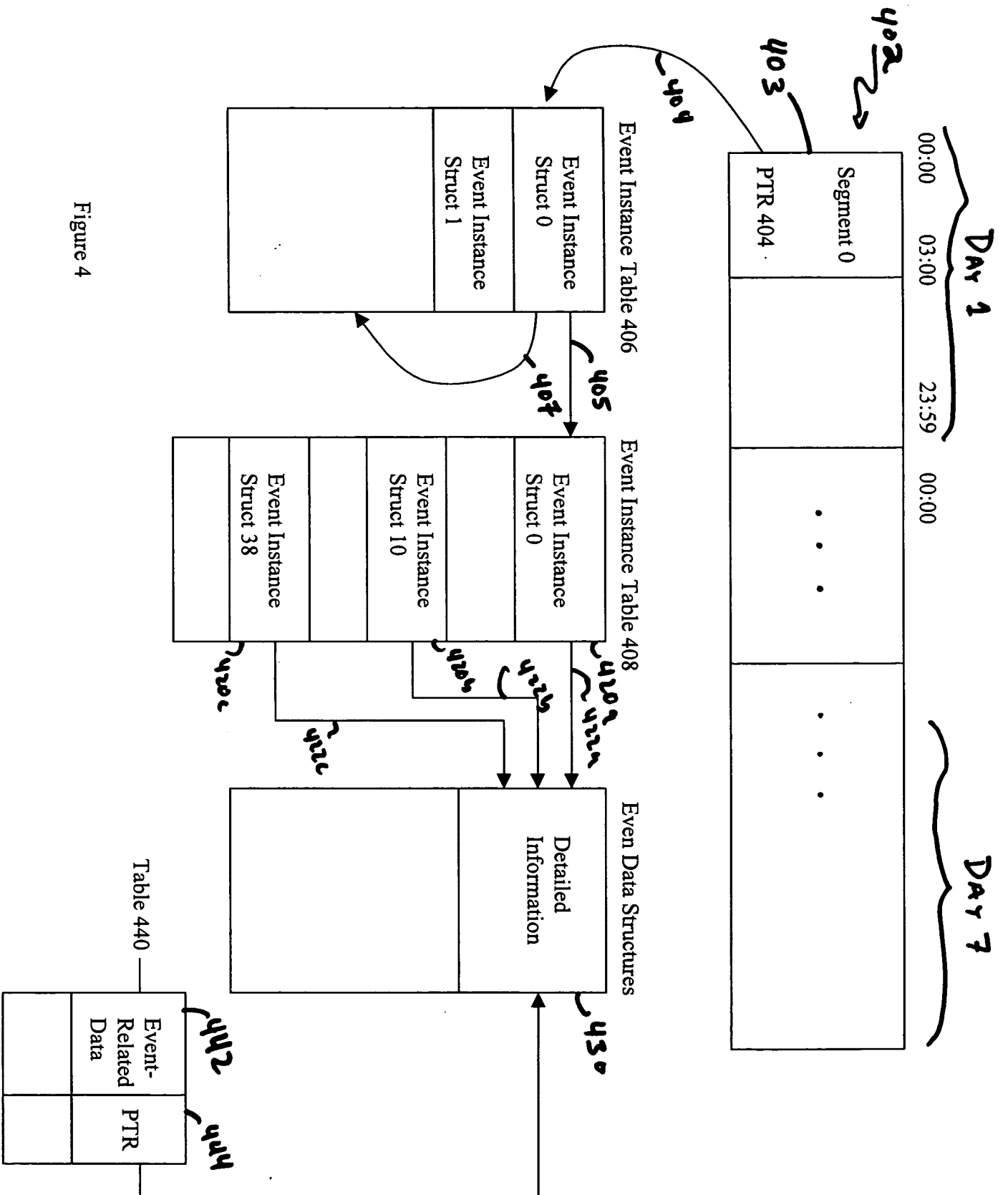


Figure 4

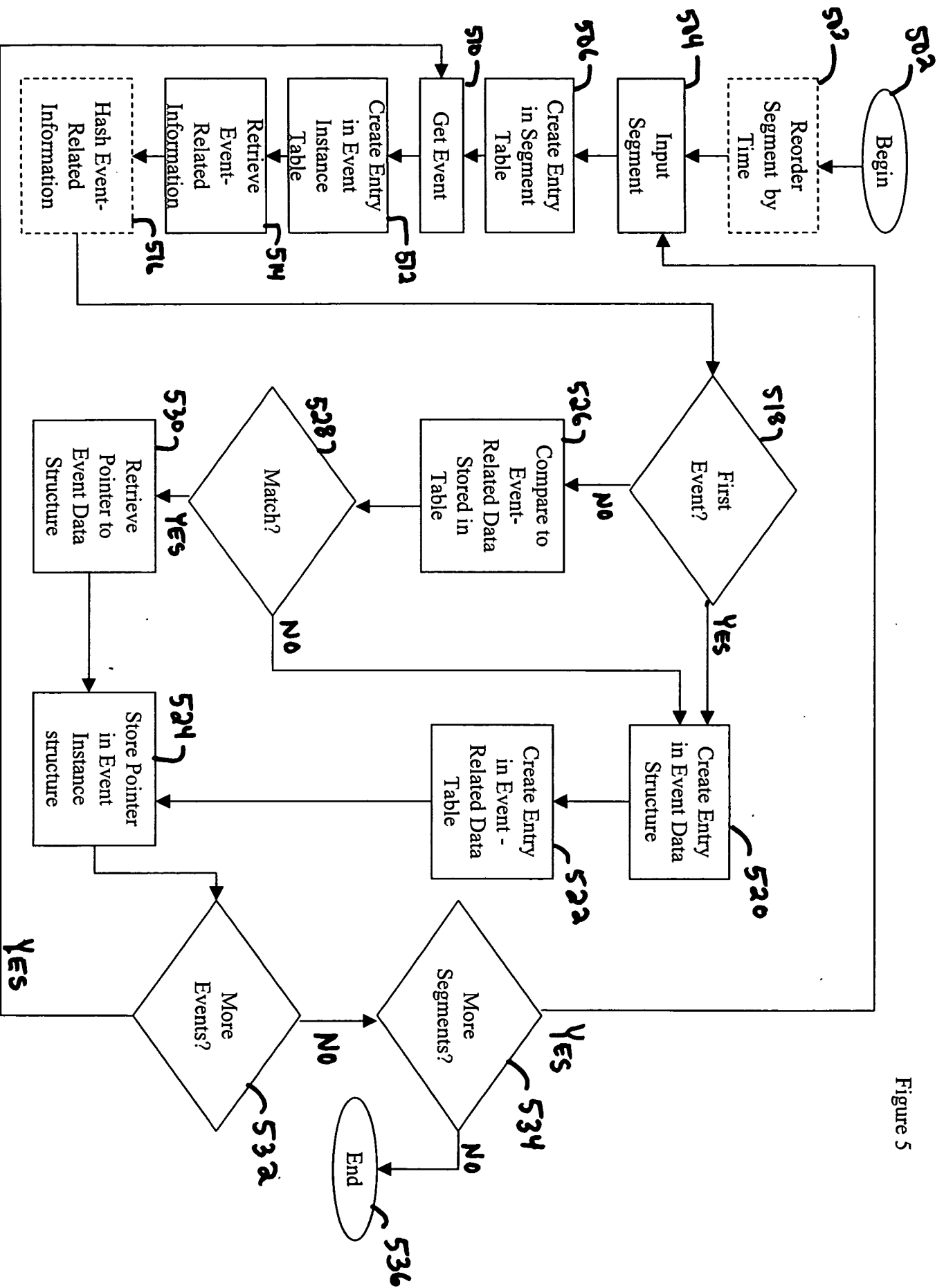


Figure 5